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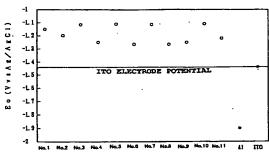
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WIRING FILM, SPUTTER TARGET FOR FORMING THE WIRING FILM AND ELECTRONIC (54)COMPONENT USING THE SAME

A wiring film comprising 0.001 to 30 atm % of a first element capable of constituting an intermetallic compound of aluminum and/or having a higher standard electrode potential than aluminum, for example, and of Y, Sc, La, Ce, Nd, Sm, Gd, Tb, Dy, Er, Th, Sr, Ti, Zr, V, Nb, Ta, Cr, Mo, W, Mn, Tc, Re, Fe, Co, Ni, Pd, Ir, Pt, Cu, Ag, Au, Cd, Si, Pb and B; and any of C, O, N and H in a proportion of 0.01 atmppm to 50 atm % of the first element, with the balance comprising substantially Al. In addition to having low resistance, such an Al wiring film can prevent the occurrence of hillocks and the electrochemical reaction with an ITO electrode. The wiring film can be obtained by sputtering in a dust-free manner by using a sputter target having a similar composition.

FIG.8



Description

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= 10

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Technical Field

The invention relates to an interconnector line of thin film suitable for forming low-resistance interconnector line, a sputter target for forming an interconnector line of thin film and electronic parts such as a liquid crystal display (LCD) and a semiconductor device applying the low-resistance interconnector line.

Background Art

An interconnector line of thin film used as a gate line and a source electrode bus-line for a TFT drive type LCD is generally produced by a sputtering method. As materials for such an interconnector line of thin film, Cr, Ti, Mo, Mo-Ta and the like have been used. But, with the enlargement of an LCD screen size, a low-resistance interconnector line of thin film is being needed. For example, a large LCD of 10 inches or more is required to have a low resistant interconnector line of 10 μ Ω cm or below. Accordingly, a low-resistance AI is attracting attention as an interconnector line of thin film for a gate line, a signal line and the like.

With the Al interconnector line of thin film, low-resistance interconnector line can be achieved, but the Al interconnector line of thin film has a problem of having protrusions called hillocks due to heating at about 473 to 773K by a heat treatment and a CVD process after interconnector line. In a process of releasing a stress of the Al film involved in heating, Al atoms spread along, for example, grain boundaries. Protrusions (hillocks) are caused involved in the spreading of Al atoms. When the protrusions are formed on the Al interconnector line, an adverse effect is caused in the subsequent process.

Therefore, it is attempted to add a very small amount of metallic elements, e.g., Fe, Co, Ni, Ru, Rh and Ir or rare earth metallic elements such as Y, La and Nd (see Japanese Patent Application Laid-Open Publication No. Hei 7-45555). Specifically, an Al interconnector line of thin film is formed using an Al target having a very small amount of such metallic elements added. The above-described metallic elements form an intermetallic compound in cooperation with Al and function as a trap material for Al. Thus, the above-described hillocks can be restrained from being occurred.

And, where the Al interconnector line is applied to source electrode bus-linesof the LCD, the Al interconnector line is stacked with an ITO electrode. When the stacked film of the Al interconnector line and the ITO electrode is immersed in an alkali solution such as a developing solution used in a patterning process, an electrochemical reaction is caused at portions where the Al interconnector line and the ITO electrode are directly contacted. The electrochemical reaction between the Al interconnector line and the ITO electrode is caused because the standard electrode potential of Al is lower than that of the ITO. Electrons are moved between the Al interconnector line and the ITO electrode due to the electrochemical reaction, resulting in a problem that the ITO electrode is colored (e.g., blackening) by being reduced and the Al interconnector line is conversely oxidized to lower electrical characteristics.

In connection with the problem due to the electrochemical reaction between the Al interconnector line and the ITO electrode described above, it is also studied to add various types of metallic elements to the Al interconnector line. In such a case, the Al target with an very small amount of metallic elements added is also used to form the Al interconnector line of thin film containing a very small amount of metallic elements.

However, the above-described Al interconnector line of thin film containing a very small amount of metallic elements suppress the diffusion of Al and the electrochemical reaction with the ITO electrode, but there are problems that the produced intermetallic compound and the added metallic elements cause an adverse effect on the etching property and sputtering property of the Al interconnector line.

Specifically, when dry etching such as CDE (chemical dry etching) and RIE (reactive ion etching) or wet etching is made on the above-described AI interconnector line of thin film, a lot of undissolved remains called residues are produced, causing a serious difficulty in forming an interconnector line network. The added metallic elements and the produced intermetallic compounds are the causes of the residues after etching as described above. And, the AI target containing the metallic elements described above causes a large amount of dust while sputtering, causing a difficulty in forming a good and fine interconnector line network.

Therefore, in the Al target and Al interconnector line of thin film used to form low-resistance interconnector line, it is a subject to restrain the occurrence of residues in etching and the occurrence of dust in sputtering in addition to suppression of the occurrence of hillocks due to the diffusion of Al and the electrochemical reaction with the ITO electrode.

And, in connection with the suppression of the electrochemical reaction between the Al interconnector line and the ITO electrode, it is desired to improve an effect upon reducing the added amount of electrical elements. The electrochemical reaction between the Al interconnector line and the ITO electrode is also studied to be suppressed by having a stacked structure of the Al interconnector line and an Mo film or the like. But, such a stacked film involves a complex LCD structure and a high cost, so that it is desired to suppress the electrochemical reaction with the ITO electrode by the Al interconnector line having a single layered structure.

The above-described subject is not limited to the Al interconnector line of thin film used for the gate line and signal line of the LCD. For example, where the Al interconnector line is applied to a typical semiconductor device such as VLSI and ULSI, electromigration is a problem. The above-described metallic elements have an effect to suppress the electromigration (see Japanese Patent Application Laid-Open Publication No. Sho 62-228446 and Japanese Patent Publication No. Hei 4-48854), but there is a problem that residues are caused in etching and dust is caused in sputtering as in the case of the LCD. And, the above-described subject is also an issue in a surface acoustic wave apparatus such as a surface acoustic wave oscillator (SAW), an electronic part using SAW (SAW device), or interconnector line and electrodes for a thermal printer head (TPH).

An object of the invention is to provide a low-resistance interconnector line of thin film with the occurrence of hill-ocks and etching residues prevented and a sputter target which can form the interconnector line of thin film with good reproducibility with the occurrence of dust in sputtering suppressed. Another object of the invention is to provide a low-resistance interconnector line of thin film which has the electrochemical reaction with the ITO or the like and the occurrence of the etching residues prevented and a sputter target with the occurrence of dust in sputtering suppressed. Besides, it aims to provide an electronic part using such a interconnector line of thin film.

Disclosure of the Invention

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A first interconnector line of thin film of the invention comprises 0.001 to 30 at% of at least one type of first element constituting an intermetallic compound of Al and 0.01 atppm to 50 at% of at least one type of second element selected from C, O, N and H with respect to the amount of the first element, with the balance comprising substantially Al.

A second interconnector line of thin film of the invention comprises 0.001 to 30 at% of at least one type of first element having a standard electrode potential higher than AI and 0.01 atppm to 50 at% of at least one type of second element selected from C, O, N and H with respect to the amount of the first element, with the balance comprising substantially AI.

Another interconnector line of thin film of the invention comprises 0.001 to 30 at% of at least one type of first element selected from Y, Sc, La, Ce, Nd, Sm, Gd, Tb, Dy, Er, Th, Sr, Ti, Zr, V, Nb, Ta, Cr, Mo, W, Mn, Tc, Re, Fe, Co, Ni, Pd, Ir, Pt, Cu, Ag, Au, Cd, Si, Pb and B and 0.01 atppm to 50 at% of at least one type of second element selected from C, O, N and H with respect to the amount of the first element, with the balance comprising substantially Al.

And, a first sputter target of the invention comprises 0.001 to 30 at% of at least one type of first element constituting an intermetallic compound of Al and 0.01 atppm to 50 at% of at least one type of second element selected from C, O, N and H with respect to the amount of the first element, with the balance comprising substantially Al.

A second sputter target of the invention comprises 0.001 to 30 at% of at least one type of first element having a standard electrode potential higher than Al and 0.01 atppm to 50 at% of at least one type of second element selected from C, O, N and H with respect to the amount of the first element, with the balance comprising substantially Al.

Another sputter target of the invention comprises 0.001 to 30 at% of at least one type of first element selected from Y, Sc, La, Ce, Nd, Sm, Gd, Tb, Dy, Er, Th, Sr, Ti, Zr, V, Nb, Ta, Cr, Mo, W, Mn, Tc, Re, Fe, Co, Ni, Pd, Ir, Pt, Cu, Ag, Au, Cd, Si, Pb and B and 0.01 atppm to 50 at% of at least one type of second element selected from C, O, N and H with respect to the amount of the first element, with the balance comprising substantially Al.

And, an electronic part of the invention is characterized by having the interconnector line of thin film of the invention. To an Al sputter target, a very small amount of at least one type of element selected from C, O, N and H is added together with an element such as Y (hereinafter described as the intermetallic compound forming element) constituting an intermetallic compound with Al or an element such as Au (hereinafter described as the high electrode potential element) having a standard electrode potential higher than Al. By these elements, an intermetallic compound, intermetallic compound forming element and high electrode potential element which exist in the obtained sputter film and are poor in etching workability are finely and uniformly precipitated in Al grains and grain boundaries.

Thus, by finely and uniformly precipitating the intermetallic compound, intermetallic compound forming element and high electrode potential electrode, the etching property is improved extensively, and the occurrence of dust in sputtering can be suppressed. And, at least one type of element selected from the added C, O, N and H does not cause an adverse effect on the effect of suppressing the diffusion of Al or the effect of suppressing the electrochemical reaction with ITO or the like. Therefore, the occurrence of hillocks can be prevented effectively by the intermetallic compound-forming element. Otherwise, the electrochemical reaction with the ITO or the like can be prevented effectively by the high electrode potential element.

As described above, the first interconnector line of thin film of the invention excels in hillock resistance and formability of a fine interconnector line network. And, the second interconnector line of thin film of the invention excels in preventing property of the electrochemical reaction with the ITO or the like and formability of the fine interconnector line network. The interconnector line of thin film of the invention can concurrently have such properties.

Brief Description of the Drawings

Fig. 1 is an equivalent circuit diagram of an embodiment having an electronic part of the invention applied to a liquid crystal display; Fig. 2 is a sectional view showing the main structure of the liquid crystal display shown in Fig. 1; Fig. 3 is a sectional view showing the main structure of one embodiment having an electronic part of the invention applied to a semiconductor device; Fig. 4 is a sectional view showing the main structure of one embodiment having an electronic part of the invention applied to a thermal printer head; Fig. 5 is a plan view showing the structure of one embodiment having an electronic part of the invention applied to a surface acoustic wave oscillator; Fig. 6 is a plan view showing the structure of another embodiment having an electronic part of the invention applied to a surface acoustic wave oscillator; Fig. 7 is a sectional view showing the main structure of an SAW device using a surface acoustic wave oscillator (SAW) shown in Figs 5 and 6; and Fig. 8 is a diagram showing electrode potential of a interconnector line of thin film of the invention in comparison with Al and ITO.

Embodiments for carrying out the Invention

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Embodiments for carrying out the invention will be described below.

A first interconnector line of thin film of the invention contains 0.001 to 30 at% of at least one type of first element constituting an intermetallic compound with AI and 0.01 atppm to 50 at% with respect to the amount of the first element of at least one type of second element selected from C, O, N and H, with the balance comprising substantially AI.

The above-described first element can be various types of elements if they constitute an intermetallic compound with Al. Specifically, they can be rare earth metallic elements such as Y, Sc, La, Ce, Nd, Sm, Gd, Tb, Dy, Er, Gd and the like, Nb, Ta, Re, Mo, W, Zr, Hf and the like.

These elements (intermetallic compound forming elements) constituting the intermetallic compound with AI form, for example, an intermetallic compound such as AI₃Y and function as a trap material for AI. Therefore, the diffusion of AI can be suppressed when thermal treatment is applied to an AI interconnector line of thin film or when the AI interconnector line of thin film is formed at a relatively high temperature. As a result, the occurrence of hillocks is prevented. And, electromigration and the like can also be suppressed.

The intermetallic compound forming elements used are preferred to have a solid solubility of 1.0 wt% or below with respect to Al. If the solid solubility with respect to Al exceeds 1.0 wt%, a sufficient effect of suppressing hillocks owing to the constitution of an intermetallic compound with Al might not be attained, and specific resistance might increase. Such intermetallic compound forming elements are Ge, Li, Mg, Th, Ti, V, Zn, W and others.

And, a second interconnector line of thin film of the invention contains 0.001 to 30 at% of at least one type of first element having a standard electrode potential higher than that of Al and 0.01 atppm to 50 at% with respect to the amount of the first element of at least one type of second element selected from C, O, N and H, with the balance comprising substantially Al.

The above-described first element of the second interconnector line of thin film can be various types of elements if they have a standard electrode potential higher than that of Al. Specifically, they can be Ag, Au, Cu, Ti, V, Nb, Ta, Cr, Mo, W, Mn, Fe, Co, Ni, Pd, Ir, Pt, Cd, Si, Pb and the like. Among them, Ag, Au, Co, Cu, Mo, W and Mn having a standard electrode potential higher by 2V (298K) or more from that of Al are preferably used in particular.

These elements (high electrode potential elements) having a higher standard electrode potential than Al can be contained in the Al interconnector line of thin film to raise a standard electrode potential of the Al interconnector line of thin film. The electrochemical reaction occurring between the Al interconnector line of thin film and the ITO electrode in an alkaline solution is a phenomenon which occurs owing to the movement of electrons because Al has a standard electrode potential lower than that of ITO. Therefore, by containing the high electrode potential elements into the Al interconnector line of thin film to increase the standard electrode potential of the Al interconnector line of thin film to exceed that of, for example, ITO, an electrochemical reaction between the Al interconnector line of thin film and the ITO electrode in the alkaline solution can be prevented. Thus, the gate line of, for example, LCD can be formed well with the Al interconnector line of thin film having a single layer structure without degrading the electrical characteristics due to coloring by reduction of the ITO electrode and oxidation of the Al interconnector line of thin film.

Improvement of the standard electrode potential of the AI interconnector line of thin film by addition of the high electrode potential elements is effective not only when it is laminated with the ITO electrode but also when the AI interconnector line of thin film is laminated with an electrode, interconnector line and the like made of various types of materials having a standard electrode potential which is higher than that of AI.

The above-described high electrode potential elements are preferably elements, which form an intermetallic compound with AI to suppress the specific resistance of the AI interconnector line of thin film from increasing. And, by using the elements constituting the intermetallic compound with AI as the high electrode potential element, the occurrence of hillocks and the electromigration can also be suppressed as described above. Thus, the elements, which have a standard electrode potential higher than that of AI and constitute an intermetallic compound with AI, are particularly effective.

The Al interconnector line of thin film containing such elements can be used satisfactorily for either the signal line or the gate line of LCD and can be said having high general-purpose properties. Such elements are Pd, V, Ni, Mo, W and Co.

The interconnector line of thin film of the invention comprises 0.001 to 50 at% of at least one type of the first element (element satisfying at least one of an intermetallic compound forming element and a high electrode potential element) selected from Y, Sc, La, Ce, Nd, Sm, Gd, Tb, Dy, Er, Th, Sr, Ti, Zr, V, Nb, Ta, Cr, Mo, W, Mn, Tc, Re, Fe, Co, Ni, Pd, Ir, Pt, Cu, Ag, Au, Cd, Si, Pb and B and 0.01 atppm to 50 at% with respect to the amount of the first element of at least one type of second element selected from C, O, N and H, with the balance comprising substantially Al.

The above-described intermetallic compound forming element and high electrode potential element are contained in a range of 0.001 to 30 at% into the Al interconnector line of thin film. For example, if the intermetallic compound forming element is contained in an amount of less than 0.001 at%, the above-described hillock suppressing effect cannot be obtained satisfactorily. On the other hand, if it exceeds 30 at%, the intermetallic compound increases the resistance of the Al interconnector line of thin film and also becomes the cause of producing residues in dry etching or wet etching. And, if the high electrode potential element is contained in an amount of less than 0.1 at%, the above-described electrochemical reaction suppressing effect cannot be obtained satisfactorily. And, if it exceeds 20 at%, the resistance of the Al interconnector line of thin film is increased, and residues are produced in dry etching or wet etching. A more preferable adding amount is in a range of 0.1 to 20 at%.

As described above, the interconnector line of thin film of the invention has a very small amount of at least one type of element selected from C, O, N and H contained together with an element which satisfies at least either the intermetallic compound forming element or the high electrode potential element. This element (C, O, N, H) contained in a very small amount effectively acts on precipitation of a very small amount of the intermetallic compound, or the intermetallic compound forming element and the high electrode potential element itself. Therefore, the intermetallic compound, the intermetallic compound forming element and the high electrode potential element in the Al interconnector line of thin film can be precipitated finely and uniformly in the Al grains and grain boundaries.

Thus, by finely and uniformly precipitating the intermetallic compound, the intermetallic compound forming element and the high electrode potential element in the AI interconnector line of thin film, the etching property is improved extensively. Therefore, when the interconnector line network is formed on the AI interconnector line of thin film by dry etching or the like, the occurrence of etching residues can be suppressed substantially. And, as described in detail afterward, when the sputtering method is applied to produce the AI interconnector line of thin film of the present invention, at least one type of element selected from C, O, N and H is also effective to suppress the produced amount of dust by sputtering. Therefore, the AI interconnector line of thin film with the content of fine dust reduced substantially can be obtained.

And, the diffusion of Al involved in heating by the heat treatment is suppressed by the formation of the intermetallic compound between the intermetallic compound forming element and Al as described above, and as a result, the occurrence of hillocks can be prevented effectively. Therefore, the first Al interconnector line of thin film of the invention excels in hillock resistance, does not cause an adverse effect on the subsequent process due to the occurrence of hillocks, and excels in forming a fine interconnector line network. And, an electrochemical reaction with the ITO electrode or the like in an alkaline solution is suppressed by containing therein the high electrode potential element. Accordingly, the second Al interconnector line of thin film of the invention excels in preventing the electrochemical reaction with the ITO electrode or the like and in forming the fine interconnector line network.

The contained amount of at least one type of element (hereinafter referred to as fine precipitating element) selected from C, O, N and H as described above shall be in a range of 0.01 atppm to 50 at% with respect to an amount of the intermetallic compound forming element or the high electrode potential element in the Al interconnector line of thin film. When the contained amount of the fine precipitating element is less than 0.01 atppm with respect to the amount of the intermetallic compound forming element or the high electrode potential element, the fine precipitating effect of the intermetallic compound, the intermetallic compound forming element or the high electrode potential element can not be obtained satisfactorily. On the other hand, if it exceeds 50 at%, an excess C, H or the like is precipitated on the Al grain boundaries or in the grains, lowering the etching property.

A more preferable contained amount of the fine precipitating element is in a range of 3 atppm to 3 at% when C is used, and 1.5 atppm to 7.5 at% when O, N and H are used. When C is used as the fine precipitating element, it is more preferable that it is in a range of 300 to 3000 atppm with respect to the intermetallic compound forming element or the high electrode potential element, and desirably in a range of 600 to 1500 atppm. When O, N and H are used as the fine precipitating element, it is more preferably in a range of 500 atppm to 1.5 at% with respect to the amount of the intermetallic compound forming element or the high electrode potential element, and desirably in a range of 300 to 1500 atppm.

Among the above-described fine precipitating elements. C is particularly effective for the fine precipitation of the intermetallic compound or the intermetallic compound forming element. Therefore, it is preferable to use C as the fine precipitating element for the first interconnector line of thin film.

And, H acts on the fine precipitation of the intermetallic compound, the intermetallic compound forming element and the high electrode potential element and on additional improvement of the standard electrode potential of the Al

interconnector line of thin film. Therefore, it is preferable to use H as the fine precipitating element for the second interconnector line of thin film. Namely, H lowers an ionization energy possessed by Al, the high electrode potential element and the intermetallic compound itself. Therefore, by using H as the fine precipitating element, the standard electrode potential of the Al interconnector line of thin film can be improved further. Otherwise, the contained amount of the high electrode potential element can be decreased. In addition, H promotes the chemical reaction in wet etching and acceleratingly promotes the reaction between an etching species (radical etc.) and the constituting elements of the Al interconnector line of thin film in dry etching, thereby contributing to improvement of the fine processing accuracy in etching.

But, if H is contained in an excessively large amount, plastic processability or the like of Al might be degraded. Therefore, if H is used as the fine precipitating element, the contained amount of H in the Al interconnector line of thin film is preferably 500 wtppm or below.

The first interconnector line of thin film of the invention can be obtained by, , forming the film by sputtering under general conditions, using for example the Al sputter target having the same composition therewith. And, the second interconnector line of thin film is also obtained in the same way.

Specifically, the sputter target used to form the first interconnector line of thin film comprises 0.001 to 30 at% of at least one type of intermetallic compound forming element constituting an intermetallic compound with AI and 0.01 atppm to 50 at% with respect to the intermetallic compound forming element of at least one type of fine precipitating element selected from C, O, N and H, with the balance comprising substantially AI. And, the sputter target used to form the second interconnector line of thin film comprises 0.001 to 30 at% of at least one type of high electrode potential element having a standard electrode potential higher than that of AI and 0.01 atppm to 50 at% with respect to the amount of the high electrode potential element of at least one type of fine precipitating element selected from C, O, N and H, with the balance comprising substantially AI.

The above-described specific contained amounts of the intermetallic compound forming element, the high electrode potential element and the fine precipitating element are as described above. Therefore, the sputter target of the invention can be a sputter target which comprises 0.001 to 30 at% of at least one type of first element (element satisfying at least either the intermetallic compound forming element or the high electrode potential element) selected from, for example, Y, Sc, La, Ce, Nd, Sm, Gd, Tb, Dy, Er, Th, Sr, Ti, Zr, V, Nb, Ta, Cr, Mo, W, Mn, Tc, Re, Fe, Co, Ni, Pd, Ir, Pt, Cu, Ag, Au, Cd, Si, Pb and B and 0.01 atppm to 50 at% of at least one type of second element (fine precipitating element) selected from C, O, N and H, with the balance comprising substantially Al.

By using such a sputter target, the interconnector line of thin film of the invention can be obtained with good reproducibility. In addition, the fine precipitating element also has an effect of suppressing the amount of dust produced by sputtering. Therefore, the AI interconnector line of thin film which is formed by sputtering using the sputter target of the invention has the contained amount of fine dust decreased substantially, excelling in forming the fine interconnector line network.

And, at least one type of fine precipitating element selected from C, O, N and H can be taken into the Al interconnector line of thin film by controlling, for example, the sputtering atmosphere and conditions. But, it is desirable that it is previously contained into the sputter target to obtain an effect of suppressing the occurred amount of dust by sputtering.

The above-described production method of the sputter target of the invention is not limited to a particular one, and it can be produced by applying a known production method such as atmosphere melting, vacuum melting, quench coagulation (e.g., spray foaming) or powder metallurgy.

For example, when the vacuum melting is applied, an element satisfying at least one of the intermetallic compound forming element and the high electrode potential element and the fine precipitating element such as C are mixed in a predetermined amount with AI, and undergone high-frequency melting in vacuum to produce an ingot. When O, N and H are used as the fine precipitating element, taking into consideration the contained amount as the impurity element, and such gas is bubbled while melting to contain in a given amount into the ingot. When O, N and H are used as the fine precipitating element, it is preferable to adopt the vacuum melting in order to control their contained amount.

And, when the spray foaming is applied, an element satisfying at least either the intermetallic compound forming element or the high electrode potential element and the fine precipitating element are mixed with Al in a given amount in the same way, undergone high-frequency melting, and sprayed by a spray to produce an ingot. When O, N and H are used as the fine precipitating element, taking into account the contained amount as the impurity element, such gas is injected when spraying to contain in a given amount into the ingot.

When the powder metallurgy is applied, the fine precipitating element and an element satisfying at least either the intermetallic compounding forming element or the high electrode potential element are mixed in a given amount with AI, and subjected to atmosphere sintering, hot press, HIP or the like to produce a sinter. When O, N and H are used as the fine precipitating element, N can be contained from N_2 atmosphere when the sinter is produced. And, as to O and H, an amount contained in AI mother material is specified. Thus, O, N and H are contained in a predetermined amount into the sinter. Among them, the quench coagulation, by which a high-purity and fine-crystalline material with a relatively high density can be obtained easily, is suitable.

Hot processing or cold processing is generally applied to the ingot obtained by melting and the sinter obtained by

powder metallurgy. And, if necessary, recrystallization heat processing and crystal orientation controlling are performed to obtain a desired sputter target. In the case of a large target, diffusion bonding or the like may be performed to obtain a target having a desired shape. But, when a large target which is used to form a large area LCD or the like is produced, it is preferable to form collectively by various types of methods in view of suppressing the occurrence of dust by sputtering.

And, when the sputter target of the invention is produced, it is preferable to have a processing rate of 50% or more by rolling, forging or the like. This is because the heat energy obtained from the above-described processing rate is effective to produce an array of conformed crystal lattices and to decrease fine internal defects. Since the required purity, composition, plane direction and the like are different depending on the desired sputter target, the production method can be determined appropriately according to the required properties.

The production method for the Al interconnector line of thin film of the invention is not limited to the above-described sputtering and can be various types of film forming methods if an Al interconnector line of thin film satisfying the above-described composition can be applied. In other words, the Al interconnector line of thin film of the invention is not limited to the sputter film but can be thin films produced by various types of film forming methods if they are the Al interconnector line of thin films satisfying the above-described composition.

The above-described Al interconnector line of thin film of the invention can be used for interconnector line, electrode and the like of various types of electronic parts. Specifically, they are a liquid crystal display (LCD) using the Al interconnector line of thin film of the invention as the gate line, signal line and the like, and semiconductor devices such as VLSI, ULSI and the like using the Al interconnector line of thin film of the invention as the interconnector line network. Besides, the Al interconnector line of thin film of the invention can also be used for a surface acoustic wave oscillator (SAW) and interconnector line of electronic parts such as an SAW device using the SAW, and a thermal printer head (TPH). The electronic parts of the invention are particularly effective for an enlarged and high-resolution LCD panel and a highly integrated semiconductor device.

Fig. 1 and Fig. 2 are diagrams showing one embodiment of a liquid crystal display using the Al interconnector line of thin film of the invention. Fig. 1 is an equivalent circuit diagram of an active matrix liquid crystal display using a reverse staggered structure TFT, and Fig. 2 is a sectional view showing the structure of the pertinent TFT.

In Fig. 1, 1 denotes a transparent glass substrate on which gate interconnector line 2 and data interconnector line 3 are disposed into the form of a matrix, A TFT 4 is formed by a-Si film at respective crossings of interconnector line. As shown in Fig. 2, the sectional structure of the TFT 4 has a gate electrode 2' made of the Al interconnector line of thin film (Al alloy film) of the invention formed on the transparent glass substrate 1. This gate electrode 2' is integrally formed by the same material and the same process as the gate electrode 2 of Fig. 1. And, after forming the gate electrode 2', an Si₃N₄ film 5 is formed as a gate insulating film thereon, a non-dope a-Si film 6 and an n⁺ type a-Si film 7 are formed thereon, and an Mo film 8 is formed thereon. Lastly, a drain electrode 3' and a source electrode 9 are formed thereon. A surface electrode 10 and a liquid crystal capacitance 11 of each pixel are connected to the source of the TFT 4.

Fig. 3 is a sectional view showing the main structure of one embodiment of a semiconductor device using the Al interconnector line of thin film of the invention. The structure of this semiconductor device will be described together with its production process.

In Fig. 3, 21 is a p-Si substrate, and heat oxidation is applied to this p-Si substrate to form a heat-oxidized film on the surface. Then, oxidation treatment is performed selectively excepting each region of source, gate and drain to form a field oxidized film 22. Then, the heat-oxidized film on the respective source and drain regions is removed by the formation of the resist film and etching treatment (PEP treatment). By this PEP treatment, a gate-oxidized film 23 is formed. Then, a resist film is formed excluding the respective source and drain regions, and impurity elements are implanted into the p-Si substrate 21 to form a source region 24 and a drain region 25. And, a silicide film 26 of Mo or W is formed on the gate-oxidized film 23.

Then, after forming an insulation film 27 made of silicate glass or the like on the entire surface of the P-Si substrate 21, the phosphorus silicate glass layer 27 on the source region 24 and the drain region 25 is removed by PEP treatment. A barrier layer 28 of TiN, ZrN, HfN or the like is formed on the source region 24 and the drain region 25 from which the phosphorus silicate glass layer 27 was removed.

Then, the Al interconnector line of thin film (Al alloy film) of the invention is formed on the entire surface, and the PEP treatment is applied to form Al interconnector line 29 having a desired shape. And, after forming an insulation film 30 made of an Si₃N₄ film or the like, an opening for bonding an Au lead 31 is formed by the PEP treatment to complete a semiconductor chip 32.

Fig. 4 is an exploded perspective view showing the structure of main parts of one embodiment of a thermal printer head using the Al interconnector line of thin film of the invention.

In Fig. 4, a heat-resistant resin layer 32 made of an aromatic polyimide resin or the like is formed on a supporting substrate 31 made of, for example, an Fe-Cr alloy. A base film 33 mainly consisting of, for example, Si and one of N or C is formed on the heat-resistant resin 32 by sputtering or the like. An exothermic resistor 34 and respective electrodes 35 and common electrodes 36 made of the Al interconnector line of thin film of the invention are formed on the base

film 33. A protective film 37 is formed to cover most of the electrodes 35, 36 and the exothermic resistor 34.

Fig. 5, Fig. 6 and Fig. 7 are diagrams showing an SAW and an SAW device using the Al interconnector line of thin film of the invention. Fig. 5 is a plan view showing the structure of an SAW according to one embodiment, Fig. 6 is a plan view showing the structure of an SAW according to another embodiment, and Fig. 7 is a sectional view showing the structure of an SAW device using the SAW.

In Fig. 5, transducers 42, 43 made of the AI interconnector line of thin film of the invention are formed separately on a piezoelectric substrate 41 made of an LiTaO₃ substrate and an LiNbO₃ substrate. Weighting is performed to change a crossing width of the electrodes of the transducer, e.g., the input transducer 42, to make a filter, e.g., a PIF filter of a color TV receiver. A non-crossed part of the weighted input transducer 42 is fully covered with the AI alloy film of the invention to make large an electrode terminal 44 of the input transducer 42. A sound absorbing material 45 is disposed in an overlaid form on at least a part of the electrode terminal 44 formed in this way and the outer part of the input transducer 42. The shape of the sound absorbing material 45 is made to have a slanted side edge so to cover most of the rear edge of the input transducer 42 and to cross slantingly with a surface acoustic wave where the incident side of the surface acoustic wave enters. Besides, a sound absorbing material 46 is also disposed on the outer side of the output transducer 43.

And, Fig. 6 is a plan view showing the SAW according to another embodiment. In Fig. 6, a transducer for transforming an input electric signal into a surface acoustic wave propagating on the piezoelectric substrate 41, for example, an interdigital electrode 47 made of a pair of comb-teeth type electrodes 47a and 47b mutually engaged, is formed on a piezoelectric substrate 41 consisting of an LiTaO₃ substrate or an LiNbO₃ substrate. This interdigital electrode 47 is made of the Al alloy film of the invention. Grading reflectors 48, 49 which is to reflect the surface acoustic wave excited by the interdigital electrode 47, and is made of the Al alloy film of the invention are formed on the piezoelectric substrate 41 at both ends of the interdigital electrode 47.

The SAW shown in Fig. 5 and Fig. 6 is used as a device shown in Fig. 7. In Fig. 7, an SAW 51 is fixed onto a chip carrier 53 made of, for example, a ceramics substrate, through an adhesive member 52, and a metallic cap 55 is put on the chip carrier 53 through a ring 54 made of a low-thermal expansion metal such as Kovar. A interconnector line pattern 56 made of the Al alloy film of the invention is formed on the chip carrier 53. The SAW 51 and the interconnector line pattern 56 are electrically connected by a bonding wire 57. And, the interconnector line pattern 56 (each independent pattern) on the chip carrier 53 is electrically connected to an interconnector line pattern 59 on the bottom side of the chip carrier 53 via a through hole 58 having gold or the like applied on its inner wall and sealed by an insulating material such as glass.

Now, specific embodiments of the Al sputter target and Al interconnector line of thin film of the invention and their evaluated results will be described.

Example 1

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First, 0.83 at% (2.7 wt%) of Y with respect to AI and 1630 atppm (220 wtppm) of C with respect to Y were added to AI, and this mixed material was undergone high-frequency melting to produce an ingot having a target composition. Cold rolling and machining were performed on this ingot to produce an AI sputter target having a diameter of 127 mm and a thickness of 5 mm.

Using the AI sputter target obtained as described above, an AI film having a thickness of 350nm was formed by spin precipitation on a glass substrate having a diameter of 5 inches under conditions of a back pressure of 1x10⁻⁴ Pa, power of 200W DC and sputter time of 3 min. The AI film was measured to evaluate on a specific resistance, a hillock density after the heat treatment (573 K) and the presence or not of etching residue. Etching for an evaluation test of the etching residue was performed using a BCl₃ + Cl₂ mixture gas as etching gas. The results are shown in Table 1.

And, as comparative examples of the present invention, an Al sputter target (comparative example 1-1) produced without adding Y and C and an Al sputter target (comparative example 1-2) produced under the same conditions as Example 1 excepting that C was not added were used to form Al films in the same way by sputtering. And, these Al films were also evaluated for properties in the same way as in Example 1. These results (after the heat treatment) are also shown in Table 1.

Table 1

-	Target con	mposition	Evaluate	d properties of Al sp	Hillock density after heat treat-			
	Amount of inter- metallic com- pound forming elements	Amount of C *1	Specific resist- ance (μ Ωcm)	Hillock density after heat treat- ment *2	Etching residue *3			
Example 1	A1-0.83at%Y	1630at. ppm	3.14	0	Non			
Comparative example 1-1	Al	Non	3.00	X	Non			
Comparative example 1-2	A1-0.83at%Y	Non	3.01	0	Yes			

^{*1:} Compared with the amount of intermetallic compound forming elements

It is apparent from Table 1 that the Al interconnector line of thin film of the invention excels in hillock resistance and etching. Thus, by using this Al interconnector line of thin film, a good fine interconnector line network can be formed with good reproducibility.

Example 2

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After producing the Al sputter targets having respective compositions shown in Table 2 in the same way as in Example 1, sputtering was performed under the same conditions as in Example 1 to produce respective Al interconnector line of thin films. These Al interconnector line of thin films were measured to evaluate their properties in the same way as in Example 1. The results are also shown in Table 2.

^{*2: (}a) = No hillock, (b) = Side hillocks in part, X= Hillocks on whole surface.

^{*3:} Non = No residue

Yes = Residues on the entire surface

Amount

of C *1

(atomic

ratio)

200ppm

580ppm

90ppm

37.8%

300ppm

1.8ppm

Non

55%

5%

12%

7%

Target composition

Amount of

forming

(atomic

ratio)

elements

A1-071%Y

A1-0.73%Y

A1-1.73%Y

A1-2.83%Y

A1-1.2%Y

A1-4%Y

A1-2%Y

A1-1.8%Y

A1-2.3%Y

A1-4%Y

A1-1.1%Y

intermetal-

lic compound

Evaluated properties of

Hillock

density

after

treat-

ment*2

heat

O

0

0

0

0

0

0

0

0

0

0

Etching

residue

*****3

Non

Non

Non

Non

Non

Non

Non

Non

Non

Yes

Yes

Al sputter film

Speci-

sis-

3.5

3.8

4.1

4.6

5.1

6.3

6.7

5.1

4.9

4.5

4.9

tance

fic re-

 $(\mu \Omega cm)$

Table 2

Example

Sam-

ple

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Example 3

example Comparative

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After producing Al targets using various types of elements instead of Y (compositions shown in Table 3) in the same way as in Example 1, respective Al interconnector line of thin films were obtained by sputtering under the same conditions as in Example 1.

These Al interconnector line of thin films were measured to evaluate their properties in the same way as in Example 1. And, the Al interconnector line of thin films of sample Nos. 16 through 21 were measured to evaluate reactivity with the ITO electrode in an alkaline solution. Reactivity with the ITO electrode in the alkaline solution was examined by a generally used electrode measuring method using silver/silver chloride electrodes as a reference electrode and ITO as the anode and respective Al alloys as the cathode. The results are also shown in Table 3.

Table 3

		Target com	position		d propert		
	Sam- ple No.	Amount of intermet-allic compound forming elements (atomic ratio)	Amount of C *1 (atomic ratio)	Speci- fic re- sis- tance (μΩcm)	Hillock density after heat treat- ment *2	Etch- ing resi- due *3	Reac- tivity with ITO *4
<u>E</u>	1	Al-1.5%Gd	2600ppm	3.8	0	Non	_
xar	2	A1-2.2%Gd	3700ppm	4.8	0	Non	-
Example	3	A1-2.6%Th	110ppm	4.0	0	Non	-
	4	A1-5.3%Th	70ppm	5.2	0	Non	-
W	5	A1-2.1%Re	220ppm	4.0	0	Non	-
	6	Al-1.9%B	170ppm	4.2	0	Non	
	7	Al-6.1%B	340ppm	5.1	0	Non	
	8	A1-4.9%Sc	90ppm	4.4	0	Non	-
	9	A1-3.7%Sc	410ppm	5.3	0	Non	-
	10	A1-1.6%Nd	20ppm	4.7	0	Non	
	11	Al-2.3%Dy	700ppm	3.6	0	Non	-
	12	A1-1.8%Dy	12%	4.3	0	Non	<u> </u>
	13	Al-1.0%Zr - 0.2%B	120ppm	3.5	0	Non	_
	14	Al- 0.8%Hf- 0.5%B	190ppm	3.8	0	Non	-
	15	A1-1.1%Y- 0.9%B	310ppm	4.2	0	Non	
	16	A1-2.6%Cu	400ppm	4.5	0	Non	0
	17	A1-4.2%Cu	580ppm	5.1	0	Non	0
	18	A1-0.9%Mn	350ppm	3.4	0	Non	0
	19	A1-9.7%V	1600ppm	5.6	0	Non	0
	20	Al- 15.1%Mn	5.1%	7.9	0	Non	Δ
	21	Al- 13.2%Ta	7.5%	8.8	0	Non	Δ

*4: \bigcirc = No reaction, \triangle = Reaction in part, \times = Reaction.

Example 4

First, material having 2.84 at% (6 wt%) of Co added to Al was undergone high-frequency melting (vacuum melting), and H_2 gas was bubbled into the melted metal to give H therein. The bubbling amount of H was set so that the amount of H in an ingot becomes 980 atppm (200 wtppm) with respect to the amount of Co. The ingot thus produced to have a target composition was undergone hot rolling and machining to obtain an Al sputter target having a diameter of 127 mm and a thickness of 5 mm.

Using the AI sputter target thus produced, an AI film having a thickness of 350nm was formed by spin precipitation on a glass substrate having a diameter of 5 inches under conditions of a back pressure of 1x10⁻⁴ Pa, power of 200W DC and sputter time of 2 min. The AI film was undergone patterning and dry etching and measured to evaluate on a specific resistance, a hillock density and the presence or not of etching residue after the heat treatment at 573 K. The results are shown in Table 4. The evaluation test of the etching residue was performed using a BCl₃ + Cl₂ mixture gas as etching gas.

And, as comparative examples against the invention, an Al sputter target (comparative example 4-1) produced with no addition of Co and H and an Al sputter target (comparative example 4-2) produced under the same conditions excepting no addition of H as in Example 4 were used to form an Al film in the same way by sputtering. And, these Al films were also evaluated on their properties in the same way as in Example 4. These results are also shown in Table 4.

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Table 4

	Target cor	mposition	Evaluated properties of Al sputter film				
	Amount of added elements (atomic ratio)	Amount of H *1 (atomic ratio)	Specific resist- ance (μ Ωcm)	Hillock density after heat treat- ment *2	Etching residue *3		
Example 4	AJ-2.84%Co	980ppm	4.3	0	. Non		
Comparative example 4-1	Al	Non	2.9	Х	Non		
Comparative example 4-2	Al-2.84%Co	Non	4.2	0	Yes		

^{*1:} With respect to the amount of added elements

As apparent from Table 4, it is clear that the Al interconnector line of thin film of example 4 excels in hillock resistance and etching. Therefore, by using this Al interconnector line of thin film, a good fine interconnector line network can be formed with good reproducibility.

Example 5

After producing Al sputter targets having respective compositions shown in Table 5 in the same way as in Example 4, sputtering was performed to produce films under the same conditions as in Example 4. The respective Al films thus produced were put in an alkaline solution (NMD-3/developing solution) to measure electrode potentials by using a reference electrode (Ag/AgCI/CI). The results are shown in Table 5 and Fig. 8.

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^{*3} Non = No residue

Yes = Residues on the entire surface

Table 5

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element

		Target composition		Standard
-	Sample No.	Amount of high electrode Amount of H to potential elements		electrode potential (V (vsAg/AgCl))
-E	1	Al-1.43at%Ni	3600at.ppm	-1.15
Examp	2	Al-1.9lat%Co	190at.ppm	-1.2
de	3	A1-2.01at%Pt	700at.ppm	-1.12
) e	4	Al-1.87at%Au	150at.ppm	-1.25
5	5	Al-2.3at%Mo	920at.ppm	-1.11
	6	Al-4.6at%W	80at.ppm	-1.27
	7	Al-3.8at%Pd	610at.ppm	-1.12
	8	Al-5.2at%Ta	110at.ppm	-1.27
	9	Al-3.3at%Ti	180at.ppm	-1.25
1	10	Al-1.2at%Ag	700at.ppm	-1.11
	11	A1-6.2at%V	300at.ppm	-1.22
tiv	mpara- ve ex- ole 5-1	Al	Non	-1.9
Con	mpara- ve ex- ple 5-2	(ITO)	_	-1.44

*1 With respect to the amount of high electrode potential

As apparent from Table 5 and Fig. 3, it is seen that the Al film containing an element having a standard electrode 40 potential higher than Al has an electrode potential higher than that of the ITO.

Example 6

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Using elements (Ir, Pt, V, Nb) having a standard electrode potential higher than Al, the Al sputter targets having the compositions shown in Table 6 were produced in the same way as in Example 4, and films were formed by sputtering under the same conditions as in Example 4 to obtain the Al interconnector line of thin films. These Al interconnector line of thin films were measured to evaluate their reactivity with the ITO electrode in an alkaline solution. The results are also shown in Table 6. Comparative example 6 in Table 6 has the contained amount of high electrode potential elements fell

outside the range of the invention.

Table 6

5		Sa	Target compo	sition	Evaluate sputter	ed propert	ies of A	1
10		Sample No.	Added amount of high electrode potential elements (atomic	Amount of H *1 (atomic ratio)	Speci- fic resis- tance (μΩ cm)	Hillock density after heat treat- ment *2	Etching residue *3	Reac- tiv- ity with ITO *4
15			ratio)	50	4.5	0	Non	0
	0 E	1 _	Al-1.9%Ir	50ppm	 	0	Non	0
ļ	Example 6	2	A1-2.4%Pt	17000ppm		0	Non	0
20	pl	3	A1-5.7%V	39000ppm			Non	0
	TO TO	4	A1-2.3%Nb	400ppm	5.1	0		
			A1-	120ppm	2.9	×	Non	×
	Compa	1	0.009%Ir		122 5	0	Yes	0
25	Compa	2	A1-32%Ir	300ppm	13.5		Non	Δ
	rat	3	A1-0.01%Pt	900ppm	3.1	Δ	Yes	0
	tive 6	4	A1-35%Pt	20ppm	12.5	0	Yes	10
	0	5	AL-33%V	2500ppm	19.9	0		$+\frac{0}{\times}$
30		6	A1-0:05%Nb	3000ppm	3.3	Δ	Non	

As apparent from Table 6, if the contained amount of high electrode potential element is excessively large, specific resistance increases, and the etching residue cannot be prevented even if an appropriate amount of H is contained. On the other hand, if the contained amount of high electrode potential elements is excessively small, the occurrence of hillocks and the reaction with the ITO electrode can not be prevented. Meanwhile, the respective Al interconnector line of thin films having an appropriate amount of high electrode potential elements and H contained according to Example 6 are seen excelling in specific resistance, hillock resistance, etching and resistance of reaction with the ITO electrode. Therefore, by using such an Al interconnector line of thin film, a good fine interconnector line network can be formed with good reproducibility. And, the gate line or the like of the LCD can also be formed properly.

Example 7

Using elements (Au, Ag, Pd) having a standard electrode potential higher than AI, the AI sputter targets having the compositions shown in Table 7 were produced in the same way as in Example 4, and films were formed by sputtering under the same conditions as in Example 4 to obtain the Al interconnector line of thin films. These Al interconnector line of thin films were measured to evaluate their properties in the same way as in Example 1. And, the etching property of the Al interconnector line of thin films was examined on the etching rate of wet etching and dry etching respectively. The

Comparative example 7 in Table 7 shows Al films formed by sputtering in the same way respectively using the Al results are shown in Table 7. sputter target produced in the same conditions as Example 7 excepting that H was not added.

Table 7

						Evaluate	ed propert	ies of	Al.
5		Sa	Ta	rget com	position	sputter	£11m		
10		Sample No.	am of el de te	ded ount high ectro- po- ential lements	Amount of H (atomic ratio *1) (tar- get/wtppm)	Speci- fic resis- tance ($\mu \Omega$ cm)	Hillock density after heat treat- ment *2	Wet etch- ing rate (nm/ min)	Dry etch ing rate (nm/ min)
			r	atomic atio)	163ppm	3.1	0	105	380
20	Exa	1	0	1- 1.2%Au 11-	(450ppm) 354ppm	4.2	0	111	400
20	Example	2).9%Ag	(380ppm) 580ppm	4.6	0	101	340
	7	3		Al- 1.2%Pd	(480ppm)	3.1	Δ	75	210
25	exam			Al- 0.7%Au	-	4.8	Δ	70	250
	12 2	arat 2	2	Al- 2.5%Ag			0	55	200
30	1	۳- ۲-	3	Al-19%E	ed -	13.2			

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As apparent from Table 7, each Al interconnector line of thin film according to Example 7 containing an appropriate amount of high electrode potential element and H excels in specific resistance and anti-hillock property and also has amount of high electrode potential element and H excels in specific resistance and anti-hillock property and also has high etching rate property. Therefore, by using this Al interconnector line of thin film, a good fine interconnector line network can be formed with good reproducibility and high efficiency.

After producing the AI sputter targets having the compositions shown in Table 8 in the same way as in Example 4, films were formed by sputtering under the same conditions as in Example 4 to obtain the respective AI interconnector line of thin films. The properties of these AI interconnector line of thin films were measured to evaluate in the same way as in Example 1. The results are shown in Table 8.

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Etch

resi

Non Non Non Non Non Non Non Yes Yes Yes Non Yes

Table 8

5			Target composi	rion	Evaluat	ed proper putter fi	ties lm
10		Sample No.	Amount of intermetallic compound forming amount (atomic	Amount of H *1 (atomic ratio)	Speci fic resis- tance ($\mu \Omega$ cm)	Hillock density after heat treat- ment *2	Etch ing res: due *3
15			ratio)	300ppm	5.4	0	Non
	Ê,	1	A1-1.4%Y	50ppm	6.9	0	Non
	Example	2	A1-3-2%Zr	720ppm	4.8	0	Non
	pl	3	A1-2.1%La	400ppm	8.8	0	Nor
20	ρ Θ	4	A1-11%Ce A1-7.9%Nd	190ppm	6.5	0	Nor
	\	5	A1-7.98Nd A1-2.28Nd	300ppm	5.3	0	Nor
		6	A1-2.28Nd A1-2.8%Nd	800ppm	5.7	0	No
25		17	A1-45%Y	210ppm	28.4	0	Ye
	ex ti C	1	A1-4381 A1-2.28Zr	0.004ppn	n 3.9	0	Ye
	ive	2	A1-51%La	30ppm	31.2	0	Ye
	ple	· -	A1-0.0005%Ce	7500ppm	3.1	×	No
30	0 1	5		15ppm	41.3	©	Ye
	L	ر لـــ					

Example 9

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First, material having 0.3 at% (2 wt%) of Ta added to Al was undergone high frequency melting (vacuum melting), and O_2 was bubbled to enter oxygen when melting. The entered amount of oxygen was set so that the amount of O in an ingot becomes 10 atppm (300 wtppm) with respect to the amount of Ta. The ingot thus produced to have a target composition was undergone hot rolling and machining to obtain an Al sputter target having a diameter of 127 mm and

Using the Al sputter target thus produced, an Al film having a thickness of 350nm was formed by spin precipitation a thickness of 5 mm. on a glass substrate having a diameter of 5 inches under conditions of a back pressure of 1x10⁻⁴ Pa, power of 200W DC and sputter time of 2 min. The Al film was undergone patterning and dry etching and measured to evaluate on a specific resistance, a hillock density and the presence or not of etching residue after the heat treatment at 573 K. The results are shown in Table 9. The evaluation test of the etching residue was performed using a BCI3 + CI2 mixture gas as etching gas.

And, as comparative examples with the invention, an Al sputter target (comparative example 9-1) produced with no addition of Ta and O and an Al sputter target (comparative example 9-2) produced under the same conditions excepting no addition of O as in Example 9 were used to form an Al film in the same way by sputtering. And, these Al films were also evaluated on their properties in the same way as in Example 9. These results are also shown in Table 9.

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Table 9

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	Torqui con	nnosition	Evaluated sputter	properties film	of Al
	Added amount of elements	Amount of O *1	<u> </u>	Hillock density after heat treat- ment *2	Etching residue *3
		10000	3.3	0	Non
Example 9	AI-0.3at%Ta			X	Non
Comparative	Al	Non	3.1		
l	11 0 0 10/ To	Non	3.2	0	Yes
Comparative example 9-2			<u> </u>		
	Comparative example 9-1 Comparative	Example 9 Al-0.3at%Ta Comparative example 9-1 Comparative example 9-2 Al-0.3at%Ta	Target composition Added amount of elements Amount of O *1 Example 9 Al-0.3at%Ta 10ppm Comparative example 9-1 Comparative example 9-2 Al-0.3at%Ta Non	Added amount of elements Amount of O *1 Specific resistance (μ Ωcm) Example 9 Al-0.3at%Ta 10ppm 3.3 Comparative example 9-1 Comparative Al-0.3at%Ta Non 3.2	Target composition Evaluated sputter properties film Added amount of elements Amount of O *1 Specific resistance (μ Ωcm) after heat treatment *2 Example 9 Al-0.3at%Ta 10ppm 3.3

*1: Compared with the amount of intermetallic compound forming elements

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As apparent from Table 9, it is seen that the Al interconnector line of thin film of Example 9 excels in anti-hillock property and etching property. Therefore, by using this AI interconnector line of thin film, a good fine interconnector line network can be formed with good reproducibility.

Example 10

Al targets (compositions shown in Table 10) using various types of elements were produced in the same way as in Example 9, films were formed by sputtering under the same conditions as in Example 9 to obtain respective Al inter-

These Al interconnector line of thin films were measured to evaluate their properties in the same way as in Example connector line of thin films. 1. And, the Al interconnector line of thin films of sample Nos. 5 and 6 were also measured to evaluate their reactivity with the ITO electrode in an alkaline solution in the same way as in Example 3. The results are shown in Table 10. Comparative example 10 in Table 10 had the added amounts of elements falling outside of the range of the invention.

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^{*2: (}a) = No hillock, (b) = Side hillocks in part, X= Hillocks on whole surface.

^{*3:} Non = No residue

Yes = Residues on the entire surface

Table 10

<u></u>		Samp	T	arget omposition		Evalu	lated	d proper	ties of	Al
		ple No.	Aiac	mount of ntermet-allic compound forming elements (atomic	Amount of 0 *1 (atomic ratio)	Spec fic resi tand (µ(s- a	dillock density after heat treat- ment *2	Etch- ing resi- due *3	Reac- tivity with ITO *4
		_		ratio) Al-1.1%Er	190ppm	4.7		0	Non	-
	Exa	1	- 1_		70ppm	16.	5	0	Non	<u> </u>
	Example	2	- 1-	A1-23%Th	920ppm	17.	3	0	Non	-
	i	3		19.2%Sr		4.2		0	Non	
	10	4		Al-1.4%Zr	90ppm	4.4		0	Non	0
5		[5	Al-1.2%Si	130ppm	5.		0	Non	0 .
		1	5	A1-1.7%Ti	250ppm	4.		0	Non	
		L	7	A1-0.9%Ti		-+-		0	Non	-
10			8	A1-0.7%Ti	5000ppm			0	Yes	-
	15	ဂ္ဂ	1	Al- 36.5%Er	20ppm	18	.2	1		
		<u>ğ</u>	2	A1-0.4%Th	0.007pr	pm 3.	2	0	Yes	
35		arat		A1-0.6%SI	_	pm 3	. 9	0	Yes	
	10	ive	3	A1-0.085	340ppm		 3.6	0	Yes	
40			4	38.1%Zr				×	Non	Δ
		example	5	Al- 0.0005%S	i 29000p	pm 3	. 4	1^		-
		e	6	Al-	450ppn	n 2	6.5	0	Yes	. 0

First, material having 0.28at% (2wt%) of Pt added to Al underwent high-frequency melting (vacuum melting) and Example 11 N_2 was bubbled to enter nitrogen while melting. The entered amount of nitrogen was determined so that the amount of N in an ingot becomes 19atppm (500wtppm) against the amount of Pt. The ingot produced to have the target composition was undergone hot rolling and machining to obtain an Al sputter target having a diameter of 127 mm and a thick-

Using the AI sputter target thus obtained, an AI film having a thickness of 350nm was formed by spin precipitation on a glass substrate having a diameter of 5 inches under conditions of a back pressure of 1x10⁻⁴ Pa, power of 200W

DC and sputter time of 2 min. The Al film was undergone patterning and dry etching and measured to evaluate on a specific resistance, a hillock density and the presence or not of etching residue after the heat treatment at 573 K. The results are shown in Table 11. The evaluation test of the etching residue was performed using a BCl₃ + Cl₂ mixture gas

As comparative examples of the invention, an Al sputter target (Comparative example 11-1) produced with no addition of Pt and N and an Al sputter target (Comparative example 11-2) produced under the same conditions as in Example 11 excepting that N was not added were used to form Al films by sputtering in the same way. And, these Al films were also evaluated on their properties in the same way as in Example 11. These results are also shown in Table 11.

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10			Table	e 11	d properties of AI spi	utter film
ſ		Target con			Hillock density	Etching residue
}		Added amount of elements	Amount of N *1	Specific resistance ($\mu \Omega \text{ cm}$)	after heat treat- ment *2	*3
15				3.4	©	Non
E	Example 11	Al-0.28at%	19ppm		X	Non
	·	Al	Non	3.1		
	Comparative example 11-1			3.2	0	Yes
· 20	Comparative example 11-2	Al-0.28at%	Non Non			

^{*1:} Compared with the amount of intermetallic compound forming elements

Yes = Residues on the entire surface

As apparent from Table 11, it is seen that the Al interconnector line of thin film of Example 11 excels in anti-hillock property and etching property. Therefore, by using this Al interconnector line of thin film, a good fine interconnector line network can be formed with good reproducibility.

Example 12

After producing Al targets using various types of elements (compositions shown in Table 12) in the same way as in Example 11, respective Al interconnector line of thin films were obtained by making films by sputtering under the same conditions as in Example 11. These Al interconnector line of thin films were measured to evaluate their properties by 35 the same procedure as in Example 1. Comparative example 12 in Table 12 had the added amount of element determined falling outside the range of the invention.

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^{*2:} ① = No hillock, ○ = Side hillocks in part, X= Hillocks on whole surface.

^{*3:} Non = No residue

Table 12

										orti	95	of
5							Eva	aluated	q b	roperti	63	<u> </u>
	Amount interme compour ing electronic (atomic electronic) EXAMPLE 1 Al-1.8 2 Al-2.2 3 Al-1.3 4 Al-2.4 5 Al-2.6 6 Al-3.6 7 Al-4. 8 Al-11 9 Al-1. 10 Al-1. 10 Al-1. 12 Ompour interme compour ing electronic elect		arget composit	composition		Al sputter :			llock	T		
10		le	i	mount of ntermetallic ompound form- ng elements atomic ratio)	of (a	ount N *1 tomic	ci re ta	fic sis- nce μΩ	der af he tr me	nsity ter at eat- nt *2	ir re du	esi- le *3
15		1	+-	1-1.8%Sc	66	60ppm	1	. 7	0			on
	Exa			11-2.2%La	3	30ppm	4	_ 4			_+-	on
	d d		1.		9	10ppm	4	.9	0			lon
20	1	<u> </u>		A1-2.4%Nd	4	20ppm	5	.1	C		_ 	lon
20	12	-	- 1	A1-2.8%Sm	17	0ppm	5	.7	(C			
				A1-3.0%Gd	1	.50ppm	7 5	. 4	(Non
		-	1		12	220ppm	15	5.9	_)		Non
25		<u> </u>			+:	290ppm	7	5.3		<u> </u>		Non
		<u> </u>			-	3000ppm	7	4.6		<u> </u>		Non
		<u> </u>				800ppm		4.8	19	<u> </u>		Non
30				A1-1.3%Sc		310ppm		18.9		الا		Yes
	12	3		A1-32%SC		700ppm	_	27.8		<u> </u>		Yes
	1 -			A1-31%La		6000ppm		3.5		×		Non
35			3	A1-0.0008%Ce		3200ppm		3.2		×		Non
			4	A1-0.0007%Nd		3000ppn		21.2		0		Yes
		ō	5_	A1-34%Sm		7400pp		22.6		0		Yes
40		example	6	A1-36%Gd		<u> </u>		19.9		0		Yes
70		夏	7	A1-21%Tb		0.003p		25.6		0		Yes
		le	8	A1-26%Dy		0.005p	Իա	123.0		J		

Industrial Applicability

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As apparent from the above-described examples, the interconnector line of thin film of the invention has low resistance and also excels in anti-hillock property, etching property, preventing property of an electrochemical reaction with ITO or the like. Therefore, by using the interconnector line of thin film of the invention, LCD's signal lines, gate lines, and very fine interconnector line network of a semiconductor device can be formed well. And, with the sputter target of the invention, the above-described low resistance interconnector line of thin film can be formed with good reproducibility, and the occurrence of dust in sputtering can be suppressed.

- An interconnector line of thin film, consisting essentially of 0.001 to 30at% of at least one type of first element con-Claims stituting an intermetallic compound of Al, 0.01 atppm to 50at% of at least one type of second element selected from C, O, N and H with respect to the amount of the first element, and the balance of Al.
- The interconnector line of thin film according to claim 1, wherein the interconnector line of thin film has the intermetallic compound of Al and the first element finely precipitated uniformly.
- The interconnector line of thin film according to claim 1, wherein the interconnector line of thin film has the first element whose solid solubility with respect to Al is 1.0wt% or below.
- An interconnector line of thin film, consisting essentially of 0.001 to 30at% of at least one type of first element having a standard electrode potential higher than AI, 0.01atppm to 50at% of at least one type of second element selected from C, O, N and H with respect to the amount of the first element, and the balance of Al. 15
 - 5. The interconnector line of thin film according to claim 4, wherein the interconnector line of thin film has the first element which is an element constituting an intermetallic compound of Al.
- The interconnector line of thin film according to claim 4, wherein the interconnector line of thin film has the second
 - The interconnector line of thin film according to claim 6, wherein the interconnector line of thin film contains the H in a range of 500wtppm or below.
- The interconnector line of thin film according to claim 4, wherein the interconnector line of thin film has at least either the first element or the intermetallic compound of Al and the first element finely precipitated uniformly. 25
- 9. An interconnector line of thin film, consisting essentially of 0.001 to 30at% of at least one type of first element selected from Y, Sc, La, Ce, Nd, Sm, Gd, Tb, Dy, Er, Th, Sr, Ti, Zr, V, Nb, Ta, Cr, Mo, W, Mn, Tc, Re, Fe, Co, Ni, Pd, Ir, Pt, Cu, Ag, Au, Cd, Si, Pb and B and 0.01atppm to 50at% of at least one type of second element selected from 30 C, O, N and H with respect to the amount of the first element, and the balance of Al.
- 10. The interconnector line of thin film according to claim 9, wherein the interconnector line of thin film has at least either the first element or the intermetallic compound of Al and the first element finely precipitated uniformly. 35
 - 11. A sputter target, consisting essentially of 0.001 to 30at% of at least one type of first element constituting an intermetallic compound of Al, 0.01atppm to 50at% of at least one type of second element selected from C, O, N and H with respect to the amount of the first element, and the balance of Al.
 - 12. A sputter target, consisting essentially of 0.001 to 30 at% of at least one type of first element having a standard electrode potential higher than Al, 0.01atppm to 50at% of at least one type of second element selected from C, O, N and H with respect to the amount of the first element, and the balance of Al.
 - 13. The sputter target according to claim 12, wherein the sputter target has the first element which is an element constituting an intermetallic compound of Al.
 - 14. The sputter target according to claim 12, wherein the sputter target has the second element which is H.

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- 15. The sputter target according to claim 14, wherein the sputter target contains the H in a range of 500wtppm or below.
 - 16. A sputter target, consisting essentially of 0.001 to 30wt% of at least one type of first element selected from Y, Sc, La, Ce, Nd, Sm, Gd, Tb, Dy, Er, Th, Sr, Ti, Zr, V, Nb, Ta, Cr, Mo, W, Mn, Tc, Re, Fe, Co, Ni, Pd, Ir, Pt, Cu, Ag, Au, Cd. Si, Pb and B, 0.01atppm to 50at% of at least one type of second element selected from C, O, N and H with respect to the amount of the first element, and the balance of Al.
 - 17. An electronic part having the interconnector line of thin film according to claim 1, claim 4 or claim 9.

FIG.1

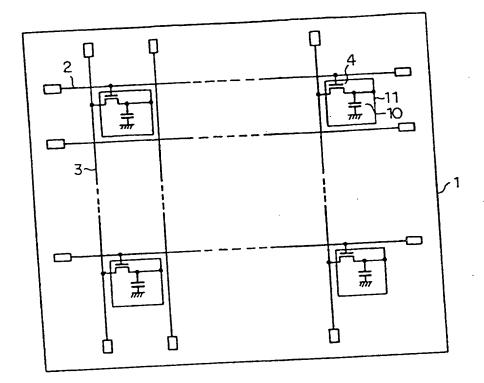


FIG.2

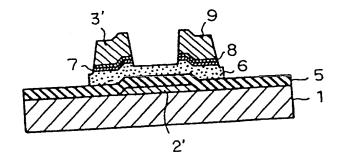


FIG.3

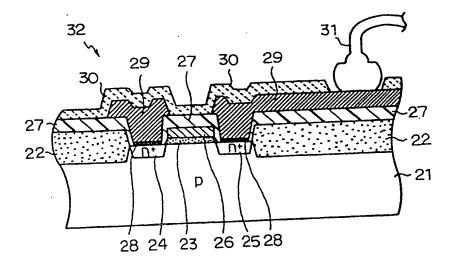


FIG.4

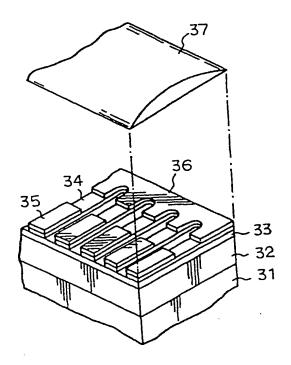


FIG.5

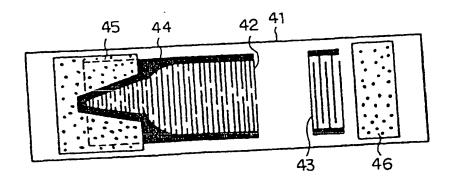


FIG.6

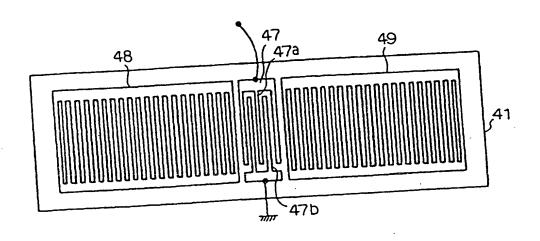


FIG.7

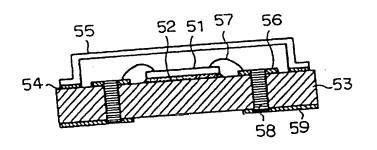
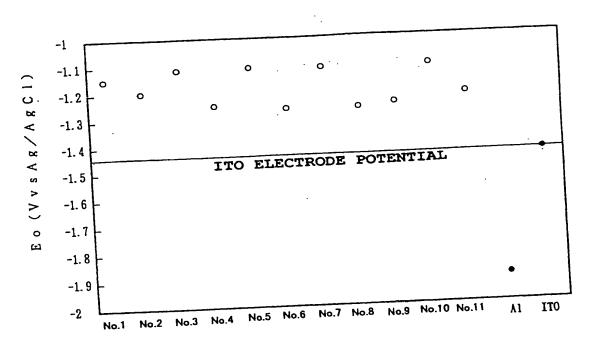


FIG.8



SAMPLES

International application No. INTERNATIONAL SEARCH REPORT PCT/JP96/02961 CLASSIFICATION OF SUBJECT MATTER C23C14/14, C23C14/34, H01L21/3205, H01L23/48 Int. Cl⁶ According to International Patent Classification (IPC) or to both national classification and IPC FIELDS SEARCHED Minimum documents tion searched (classification system followed by classification symbols) C22C21/00, C23C14/14, C23C14/34, H01L21/205, Int. Cl⁶ H01L21/3205, H01L23/48 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1926 - 1996 Kokai Jitsuyo Shinan Koho 1971 - 1996 Toroku Jitsuyo Shinan Koho 1994 - 1996 Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) WPI/L (DIAROG) C. DOCUMENTS CONSIDERED TO BE RELEVANT Relevant to claim No. Citation of document, with indication, where appropriate, of the relevant passages 1-10, 17 Category* JP, 7-45555, A (Kobe Steel, Ltd.), February 14, 1995 (14. 02. 95) & US, 5514909, A 1-10, 14-15, JP, 62-235454, A (Japan Energy Corp.), October 15, 1987 (15. 10. 87) Page 3, right column, upper part, line 18 to 17 11-13, 16 Y lower part, line 7; page 4, tabel 1 Х (Family: none) 1-10, 14-15, JP, 62-240738, A (Japan Energy Corp.), October 21, 1987 (21. 10. 87), Page 3, right column, lower part, line 20 to page 4, right column, upper part, line 12; page 4, table 1 (Family: none) 17 11-13, 16 x 6-7, 14-15 JP, 5-171434, A (Casio Computer Co., Ltd.), July 9, 1993 (09. 07. 93) & EP, 542271, A & US, 5367179, A Y 14 - 15 JP, 5-239635, A (Shimadzu Corp.), See patent family annex. X Further documents are listed in the continuation of Box C. later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention Special categories of cited documents: document defining the general state of the art which is not considered to be of particular relevance "X" document of particular relevance; the claimed invention cannot be considered as a inventive step when the document is taken alone "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "O" document referring to an oral disclosure, use, exhibition or other means document published prior to the international filing date but later than the priority date claimed "&" document member of the same patent family Date of mailing of the international search report Date of the actual completion of the international search January 14, 1997 (14. 01. 97) January 7, 1997 (07. 01. 97) Authorized officer Name and mailing address of the ISA/ Japanese Patent Office

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PCT/JP96/02961

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